

L Number	Hits	Search Text	DB	Time stamp
1	228	709/216,215,217,229,245.ccls. and ((hierarch\$4 tree same structure) same (memory storage CAM TCAM RAM))	USPAT; EPO; DERWENT; IBM_TDB	2004/01/12 17:08
2	15	709/216,215,217,229,245.ccls. and ((hierarch\$4 tree same structure) same (memory storage CAM TCAM RAM)).ab.	USPAT; EPO; DERWENT; IBM_TDB	2004/01/12 17:12
3	2	709/216,215,217,229,245.ccls. and ((hierarch\$4 tree same structure) same (memory storage CAM TCAM RAM)).ti.	USPAT; EPO; DERWENT; IBM_TDB	2004/01/12 17:13
4	38	(709/216,215,217,229,245.ccls. or 711/117,112.ccls.)and ((hierarch\$4 tree same structure) same (memory storage CAM TCAM RAM)).ti.	USPAT; EPO; DERWENT; IBM_TDB	2004/01/12 17:28
5	36	((709/216,215,217,229,245.ccls. or 711/117,112.ccls.)and ((hierarch\$4 tree same structure) same (memory storage CAM TCAM RAM)).ti. ) not (709/216,215,217,229,245.ccls. and ((hierarch\$4 tree same structure) same (memory storage CAM TCAM RAM)).ti. )	USPAT; EPO; DERWENT; IBM_TDB	2004/01/12 17:14
6	71	(711/117,112.ccls.)and ((hierarch\$4 tree same structure) same (memory storage CAM TCAM RAM)).ab.	USPAT; EPO; DERWENT; IBM_TDB	2004/01/12 17:28
7	38	Cheriton.in.	USPAT; EPO; DERWENT; IBM_TDB	2004/01/12 17:29
-	8	hierarch\$4 same (memory storage CAM) and classif\$4 and message and (match\$3 compare\$3) and (ACL)	USPAT	2004/01/10 14:14
-	245	hierarch\$4 same (memory storage CAM TCAM RAM).ti.	USPAT	2004/01/10 14:15
-	245	hierarch\$4 same (memory storage CAM TCAM RAM).ti.	USPAT	2004/01/10 15:08
-	128	hierarch\$4 same (memory storage CAM TCAM RAM).ti. and (709/\$.ccls. or 711/\$.ccls.)	USPAT	2004/01/10 15:01
-	6	("5778435"   "6237074"   "6292871"   "6308242"   "6385719"   "6473837").PN.	USPAT	2004/01/10 14:29
-	1	hierarch\$4 same (memory storage CAM TCAM RAM).ti. and (709/\$.ccls. or 711/\$.ccls.) and (network same messag\$3)	USPAT	2004/01/10 14:30
-	4	hierarch\$4 same (memory storage CAM TCAM RAM).ti. and (709/\$.ccls. or 711/\$.ccls.) and associative and CAM	USPAT	2004/01/10 15:01
-	1104	hierarch\$4 same (memory storage CAM TCAM RAM).ti.	USPAT; US-PGPUB; EPO; DERWENT; IBM_TDB	2004/01/10 15:14
-	1	hierarch\$4 same (memory storage CAM TCAM RAM).ti. and acl and (network same (message signal data commands request))	USPAT; US-PGPUB; EPO; DERWENT; IBM_TDB	2004/01/10 15:10
-	859	hierarch\$4 same (memory storage CAM TCAM RAM).ti. not (hierarch\$4 same (memory storage CAM TCAM RAM).ti.)	USPAT; US-PGPUB; EPO; DERWENT; IBM_TDB	2004/01/10 15:15
-	859	hierarch\$4 same (memory storage CAM TCAM RAM).ti.	US-PGPUB; EPO; DERWENT; IBM_TDB	2004/01/10 15:15

-	819	hierarch\$4 same (memory storage CAM TCAM RAM).ti.	EPO; DERWENT; IBM_TDB	2004/01/10 15:25
-	0	hierarch\$4 same (memory storage CAM TCAM RAM).ti. and CAM and ACL and (match\$3 compare\$4)	EPO; DERWENT; IBM_TDB	2004/01/10 15:26
-	0	hierarch\$4 same (memory storage CAM TCAM RAM).ti. and ACL and (match\$3 compare\$4)	EPO; DERWENT; IBM_TDB	2004/01/10 15:27
-	0	hierarch\$4 same (memory storage CAM TCAM RAM).ti. and (access adj control adj list) and (match\$3 compare\$4)	EPO; DERWENT; IBM_TDB	2004/01/10 15:28
-	0	hierarch\$4 same (memory storage CAM TCAM RAM).ti. and (access adj control adj list)	EPO; DERWENT; IBM_TDB	2004/01/10 15:28
-	0	hierarch\$4 same (memory storage CAM TCAM RAM).ti. and (acl)	EPO; DERWENT; IBM_TDB	2004/01/10 15:29
-	819	(hierarch\$4 same (memory storage CAM TCAM RAM)).ti.	EPO; DERWENT; IBM_TDB	2004/01/10 15:29
-	245	(hierarch\$4 same (memory storage CAM TCAM RAM)).ti.	USPAT	2004/01/10 15:29
-	8	(hierarch\$4 same network same (memory storage CAM TCAM RAM)).ti.	USPAT	2004/01/10 17:14
-	45	(hierarch\$4 same network same (memory storage CAM TCAM RAM)).ti.	EPO; DERWENT; IBM_TDB	2004/01/10 16:30
-	8	(hierarch\$4 same network same (memory storage CAM TCAM RAM)).ti.	USPAT	2004/01/10 17:09
-	0	(hierarch\$4 same network same (memory storage CAM TCAM RAM)).ti. and assoicative and classification	USPAT	2004/01/10 17:10
-	0	(hierarch\$4 same network same (memory storage CAM TCAM RAM)).ti. and assoicative and classif\$7	USPAT	2004/01/10 17:10
-	0	(hierarch\$4 same network same (memory storage CAM TCAM RAM)).ti. and assoicative and classif\$7	US-PGPUB; EPO; DERWENT; IBM_TDB	2004/01/10 17:10
-	0	(hierarch\$4 same network same (memory storage CAM TCAM RAM)).ti. and assoicative	US-PGPUB; EPO; DERWENT; IBM_TDB	2004/01/10 17:11
-	0	(hierarch\$4 same network same (memory storage CAM TCAM RAM)).ti. and associative	US-PGPUB; EPO; DERWENT; IBM_TDB	2004/01/10 17:11
-	0	(hierarch\$4 same network same (memory storage CAM TCAM RAM)) same associate\$3 same acl	USPAT	2004/01/10 17:17
-	15	(hierarch\$4 same network same (memory storage CAM TCAM RAM)) and associate\$3 and acl	USPAT	2004/01/10 17:20
-	25	(hierarch\$4 near3 (memory storage CAM TCAM RAM)) and network and acl	USPAT	2004/01/10 17:40
-	385	(hierarch\$4 near3 (memory storage CAM TCAM RAM)).ab.	USPAT	2004/01/10 17:41
-	0	(hierarch\$4 near3 (memory storage CAM TCAM RAM)).ab. and ACL	USPAT	2004/01/10 17:42
-	1	(hierarch\$4 near3 (memory storage CAM TCAM RAM)).ab. and ((access adj control adj list) or ACL)	USPAT	2004/01/10 17:42
-	5	(hierarch\$4 same (memory storage CAM TCAM RAM)).ab. and ((access adj control adj list) or ACL)	USPAT	2004/01/10 17:46
-	589	(hierarch\$4 same (memory storage CAM TCAM RAM)) and (strings data messages) and classify\$6	USPAT	2004/01/10 17:47

-	51	(hierarch\$4 same (memory storage CAM TCAM RAM)).ab. and (strings data messages) and classify\$6	USPAT	2004/01/10 17:47
-	8	(hierarch\$4 same (memory storage CAM TCAM RAM)).ti. and (strings data messages) and classify\$6	USPAT	2004/01/10 18:03
-	58	(hierarch\$4 same (memory storage CAM TCAM RAM)) and (strings data messages) same fields same match\$3 and classify\$6	USPAT	2004/01/10 18:04
-	4	(hierarch\$4 same (memory storage CAM TCAM RAM)).ab. and (strings data messages) same fields same match\$3 and classify\$6	USPAT	2004/01/10 18:24
-	6101	(hierarch\$6 same (memory storage CAM TCAM RAM cache (content adj addressable adj memory))) and (strings data messages field) same (match\$4 compare\$4)	USPAT	2004/01/10 18:27
-	3014	(hierarch\$6 near4 (memory storage CAM TCAM RAM cache (content adj addressable adj memory))) and (strings data messages field) same (match\$4 compare\$4)	USPAT	2004/01/10 18:28
-	977	(hierarch\$6 adj (memory storage CAM TCAM RAM cache (content adj addressable adj memory))) and (strings data messages field) same (match\$4 compare\$4)	USPAT	2004/01/10 18:28
-	3	((hierarch\$6 adj (memory storage CAM TCAM RAM cache (content adj addressable adj memory))) and (strings data messages field) same (match\$4 compare\$4)).ab.	USPAT	2004/01/10 18:30
-	0	((hierarch\$6 adj (memory storage CAM TCAM RAM cache (content adj addressable adj memory))) and (strings data messages field) same (match\$4 compare\$4)).ti.	USPAT	2004/01/10 18:30
-	43	((hierarch\$6 adj (memory storage CAM TCAM RAM cache (content adj addressable adj memory))).ti. and (strings data messages field) same (match\$4 compare\$4))	USPAT	2004/01/10 19:58
-	8	((hierarch\$6 adj (memory storage CAM TCAM RAM cache (content adj addressable adj memory))).ti. and (strings data messages field) same (match\$4 compare\$4))	EPO; DERWENT; IBM_TDB	2004/01/10 19:58
-	57727	((hierarch\$4 tree same structure) same (memory storage CAM TCAM RAM)).ti. and (match\$3 compare\$4) and DNS or IP	USPAT; EPO; DERWENT; IBM_TDB	2004/01/12 14:34
-	22	((hierarch\$4 tree same structure) same (memory storage CAM TCAM RAM)).ti. and (match\$3 compare\$4) and (DNS or IP)	USPAT; EPO; DERWENT; IBM_TDB	2004/01/12 17:02


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*Chiang, C.-Y.; Liu, M.T.; Muller, M.E.;*

Parallel Processing, 1999. Proceedings. 1999 International Conference on , 21 Sept. 1999

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**2 Storage hierarchies: Gaps, cliffs, and trends**

*Pugh, E.;*

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**3 Traffic analysis of a Web proxy caching hierarchy**

*Mahanti, A.; Williamson, C.; Eager, D.;*

Network, IEEE , Volume: 14 , Issue: 3 , May-June 2000

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**4 A 1-V, 100-MHz, 10-mW cache using a separated bit-line memory hierarchy architecture and domino tag comparators**

*Mizuno, H.; Matsuzaki, N.; Osada, K.; Shinbo, T.; Ohki, N.; Ishida, H.; Ishiba K.; Kure, T.;*

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**5 On request forwarding for dynamic Web caching hierarchies**

*Cho-Yu Chiang; Yingjie Li; Liu, M.T.; Muller, M.E.;*

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*Fritts, J.; Wolf, W.;*

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*Chen Ding; Chi-Hung Chi; Tam, V.;*

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*Turgeon, P.R.; Pak-Kin Mak; Plass, D.; Blake, M.; Fee, M.; Fischer, M.; Ford, Holmes, G.; Jackson, K.; Jones, C.; Kark, K.; Malgioglio, F.; Meaney, P.; Pell, Scarpero, W.; Seigler, A.R.; Shen, W.; Strait, G.; Vanhuben, G.; Wellwood, G Zuckerman, A.;*

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*Meira, W., Jr.; Fonseca, E.; Murta, C.; Almeida, V.;*

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*Johnson, T.L.; Wen-mei W. Hwu;*

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16 **Efficient simulation methods for multi-level cache memory hierarch**  
*Si-En Chang; Chia-Chang Hsu;*  
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17 **Re-defining the storage hierarchy: an ultra-fast magneto-optical di drive**  
*Nakagomi, T.; Holzbach, M.; Van Meter, R., III; Ranade, S.;*  
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*Nilsson, A.A.; Khanmoradi, H.;*  
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*Wen-Hann Wang; Baer, J.; Levy, H.M.;*  
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*Mizuno, H.; Matsuzaki, N.; Osada, K.; Shinbo, T.; Ooki, N.; Ishida, H.; Ishiba K.; Kure, T.;*

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